



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alfred Haeusler et al.

Art Unit: 2812

Serial No.: 10/824,745

Examiner: TBD

Filed: 04/15/2004

Docket No.: TID-34858

Customer No.: 23494

Conf. No.: 7054

For: METHOD OF FABRICATING AN INTEGRATED SILICON-GERMANIUM
HETEROBIPOLAR TRANSISTOR AND AN INTEGRATED SILICON-GERMANIUM
HETEROBIPOLAR TRANSISTOR

ART CITE

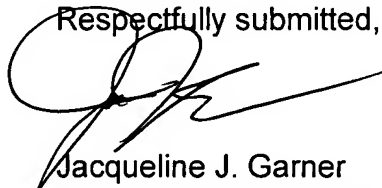
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Dear Sir:

Please make the references listed on the enclosed PTO-1449 of record under 37 C.F.R. 1.56, 1.97, and 1.98 in the patent application identified above. They were cited in a corresponding German patent application. Copies of the listed references are enclosed.

Respectfully submitted,


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		Complete If Known		
		Application Number	10/824,745	
		Filing Date	4/15/2004	
		First Named Inventor	Alfred Haeusler	
		Group Art Unit	2812	
		Examiner Name	TBD.	
Sheet	1	1	Attorney Docket No.	TID-34858

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	4,672,413		Gardner	6/9/1987	
	AB					
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FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ² (if known)				
	BA	PCT	WO 02/41361 A2		Joshi et al.	5/23/2002		
	BB	DE	197 52 052 A1		Sanders et al.	8/6/1998		
	BC	DE	690 22 692 T2		Morishita	3/14/1996		
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